

FIGURE 1 (PRIOR ART)

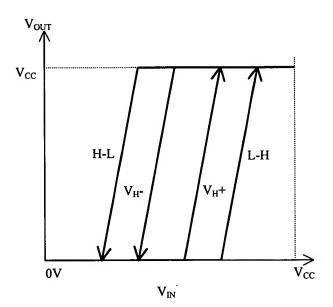


FIGURE 2 (PRIOR ART)

Attorney's Docket No.: 110348-133032 Date of Inventor(s): Nazar Syed Haider et al App For: Reference Voltage Generator for Hysteresis Circuit Express Mail No. EL973636795US

Date of Deposit: September 19, 2003 Application No.: Not yet assigned

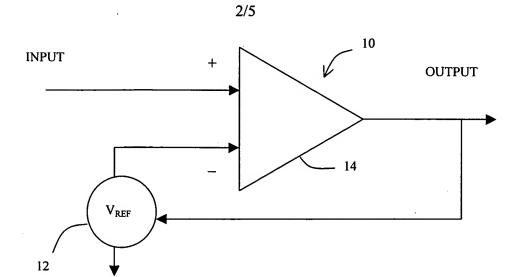


FIGURE 3 (PRIOR ART)

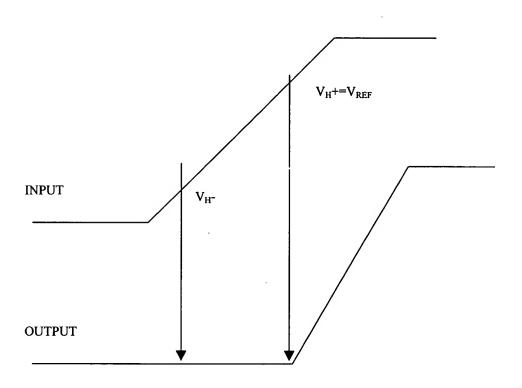


FIGURE 4 (PRIOR ART)

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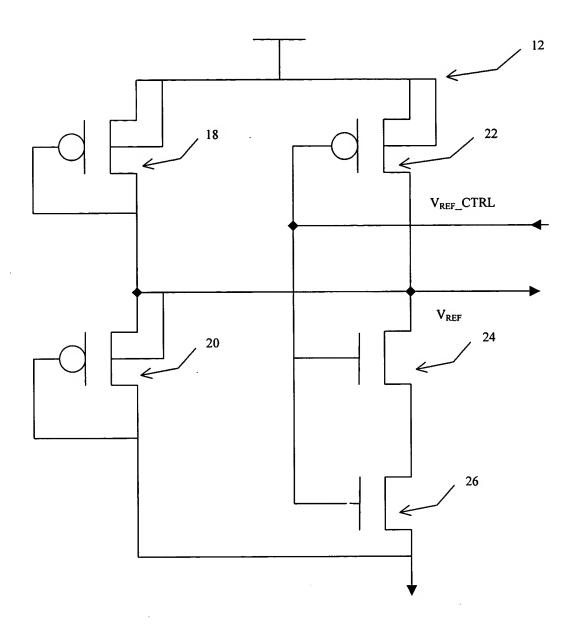


FIGURE 5 (PRIOR ART)

Attorney's Docket No.: 110348-133032 Date of Deposit: September 19, 2003 Inventor(s): Nazar Syed Haider et al Application No.: Not yet assigned For: Reference Voltag Generator for Hysteresis Circuit Express Mail No. EL973636795US

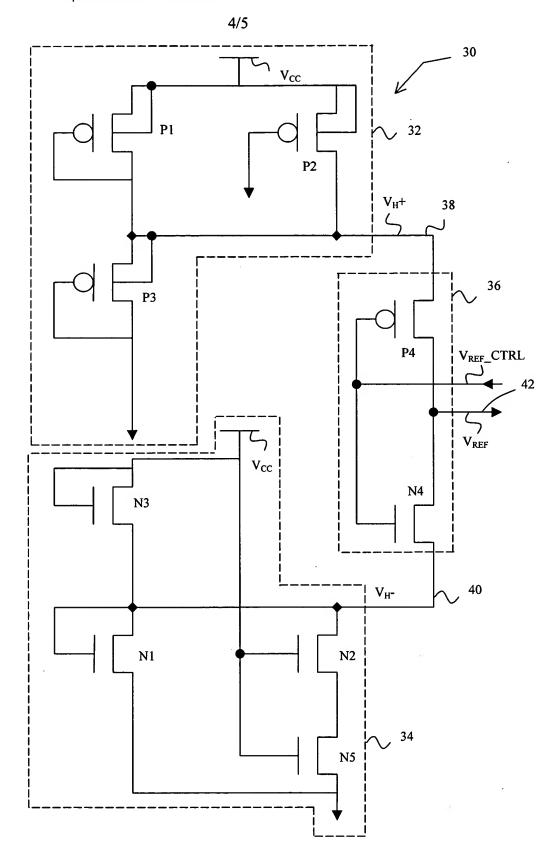


FIGURE 6

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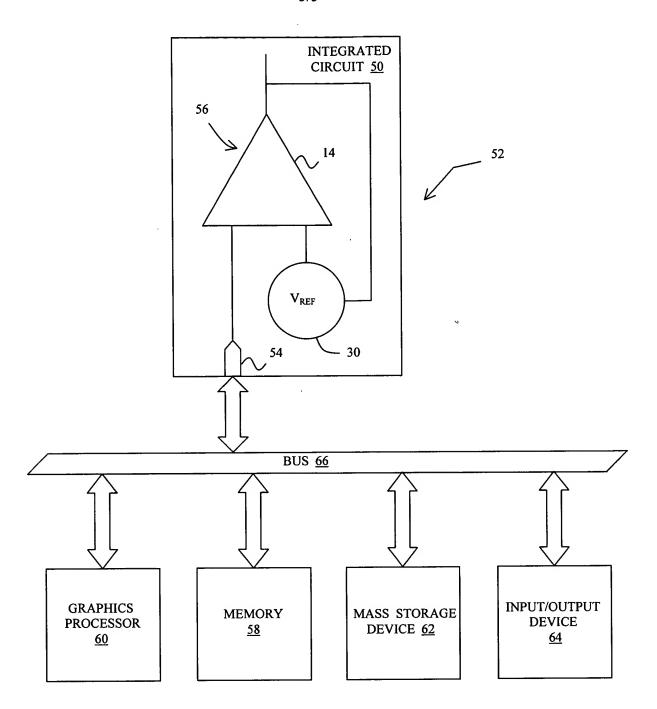


FIGURE 7